IN THE CLAIMS:

Please amend claims 5, 6, 11 and 12 as follows. This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-4 (canceled).

Claim 5 (Currently Amended): A multilayer printed wiring board comprising:

a substrate[[,]];

a plated through-hole[[,]] formed in the substrate;

a solvent-free insulative filling material ~~filling~~ filled in the plated through-hole; ~~, and~~

a conductor layer f~~ormed~~ plated on an exposed surface of the solvent-free insulative filling material ~~in the plated through-hole,~~;

an insulating layer formed on a surface of the conductor layer;

a conductive pattern layer formed on a surface of the insulating layer; and

a via conductor connecting the conductor layer and the conducting pattern layer;

wherein the solvent-free insulative filling material includes a filler, a thermosetting epoxy resin~~, a curing agent, and~~ a curing catalyst~~, the thermosetting resin being an epoxy resin, the curing agent being~~ and a dicyandiamide curing agent.

Claim 6 (Currently Amended): The multilayer printed wiring board according to claim 5, wherein ~~which further comprises: an insulating layer formed on a surface of the conductor layer; a conductor pattern layer formed on a surface of the insulating layer so that~~ the conductor layer, the insulating layer and conductor pattern layer are provided in this order~~; and a via conductor which electrically connects the conductor layer and the conductor pattern layer~~.

Claim 7 (Original): The multilayer printed wiring board according to claim 5, wherein the plated through-hole has a diameter of 200 μm or smaller.

Claims 8-9 (Canceled).

Claim 10 (Previously Presented): The multilayer printed wiring board according to claim 5, wherein the curing catalyst comprises a urea compound.

Claim 11 (Currently Amended): The multilayer printed wiring board according to claim 10, wherein the urea compound is a material selected from the ~~ground~~ group consisting of dimethylurea compound, aromatic urea compound, alicyclic urea compound and ~~hologenated~~ halogenated urea compound.

Claim 12 (Currently Amended): The multilayer printed wiring board according to claim 10, wherein the urea compound is a material selected from the ~~ground~~ group consisting of dimethylurea compound, aromatic urea compound and alicyclic urea compound.

Claim 13 (Previously Presented): The multilayer printed wiring board according to claim 5, wherein the dicyandiamide curing agent has at least one form selected from the group consisting of powders, dendrites, and flakes.

Claim 14 (Previously Presented): The multilayer printed wiring board according to claim 13, wherein the dicyandiamide curing agent is powder having an average particle size of 0.1 to 100 μm.

Claim 15 (Previously Presented): The multilayer printed wiring board according to claim 13, wherein the dicyandiamide curing agent is powder having an average particle size of 1 to 30 μm.

Claim 16 (Previously Presented): The multilayer printed wiring board according to claim 13, wherein the dicyandiamide curing agent is powder having an average particle size of 1 to 15 μm.

Claim 17 (Previously Presented): The multilayer printed wiring board according to claim 5, wherein the filler is substantially spherical particles having an average particle size of 0.1 to 12 μm and a maximum particle size of 75 μm or smaller.